

(Use several sheets if necessary)

NSI-03-001

10/796,386

Jun Ho Cho et al.

03/09/04

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JZ	R. Senthinathan et al., "Application Specific CMOS Output Driver Circuit Design Techniques to Reduce Simultaneous Switching Noise", IEEE Journal of Solid-State Circuits, Vol. 28, No. 12, Dec. 1993, pp. 1383-1388.
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Jeffrey Zweizig

3/21/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.